

FIG. 1

FIG. 3A

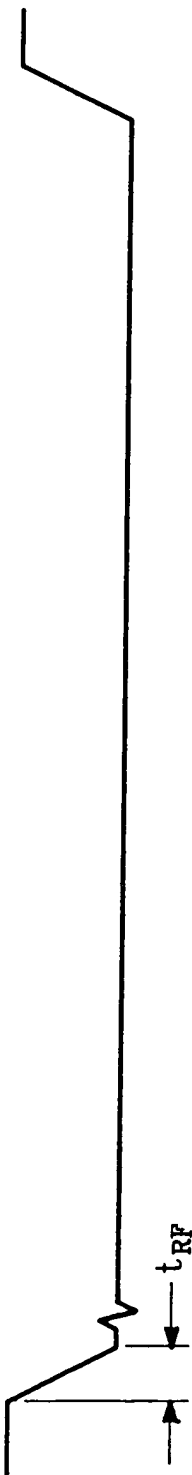


FIG. 3B

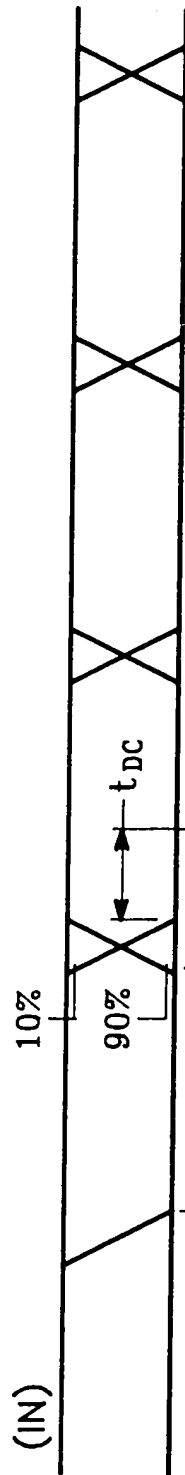
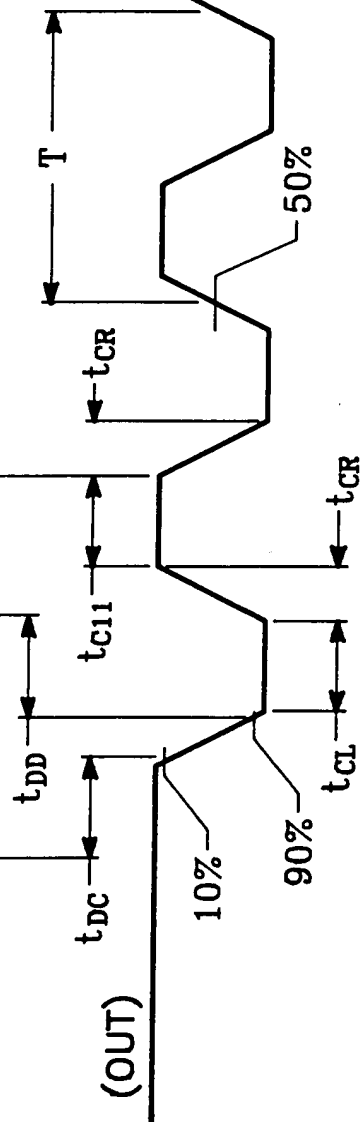


FIG. 3C



0950530 061700

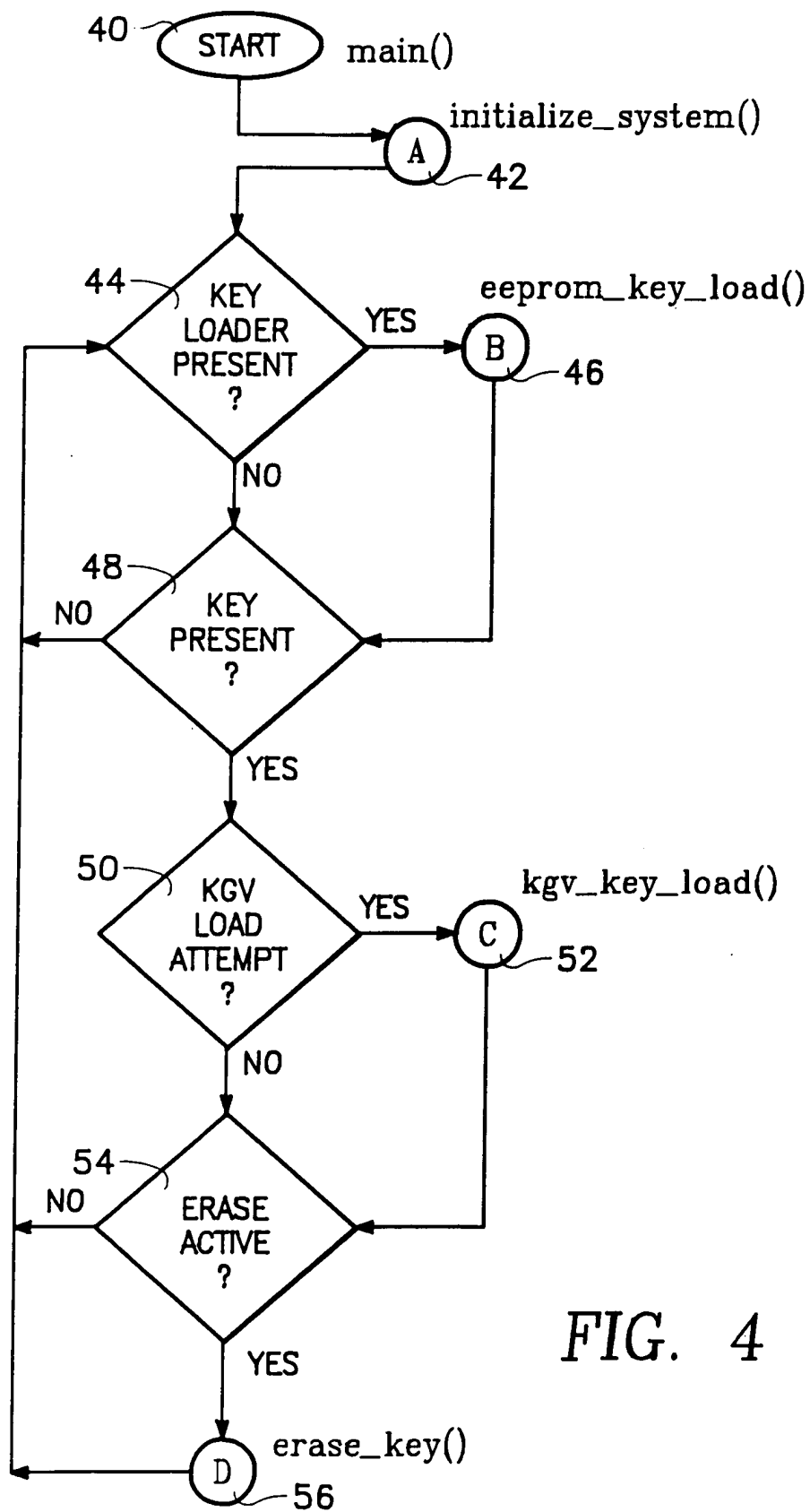


FIG. 4

EEPROM_KEY_LOAD - 02100

EEPROM_KEY_LOAD()

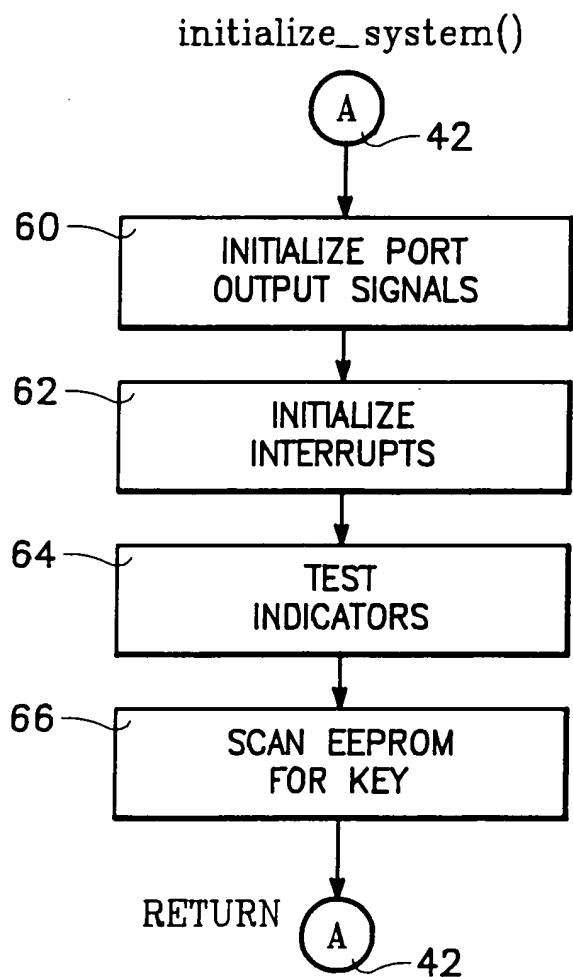


FIG. 5

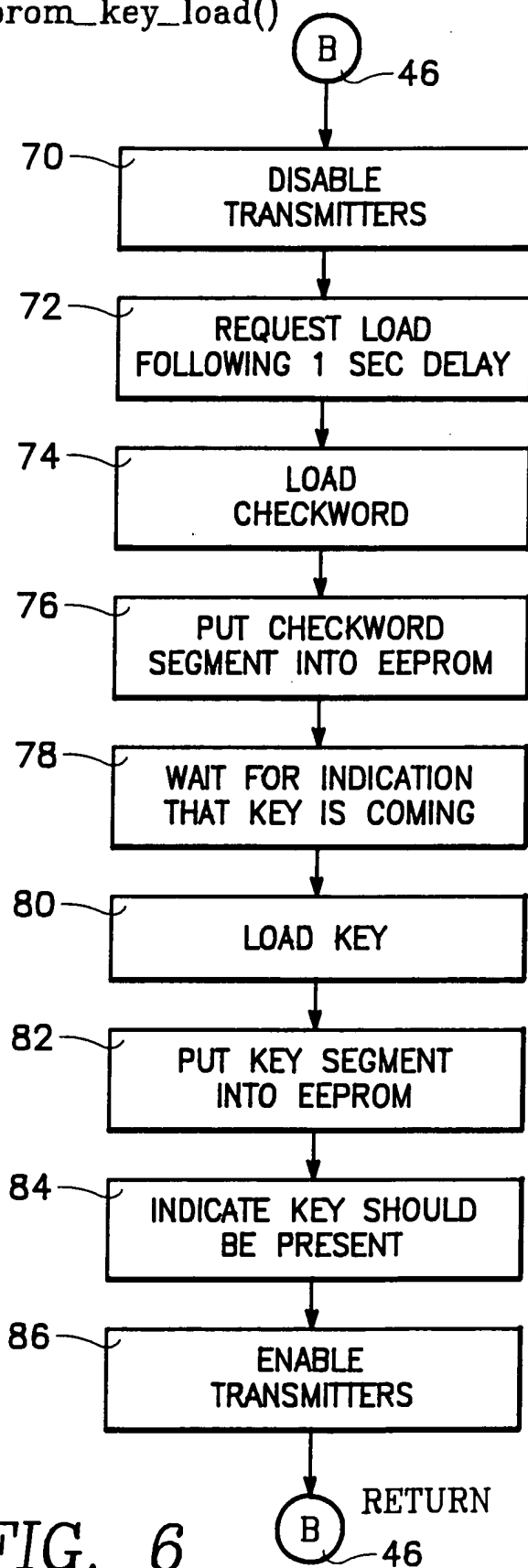


FIG. 6

09507B30: 0E1700

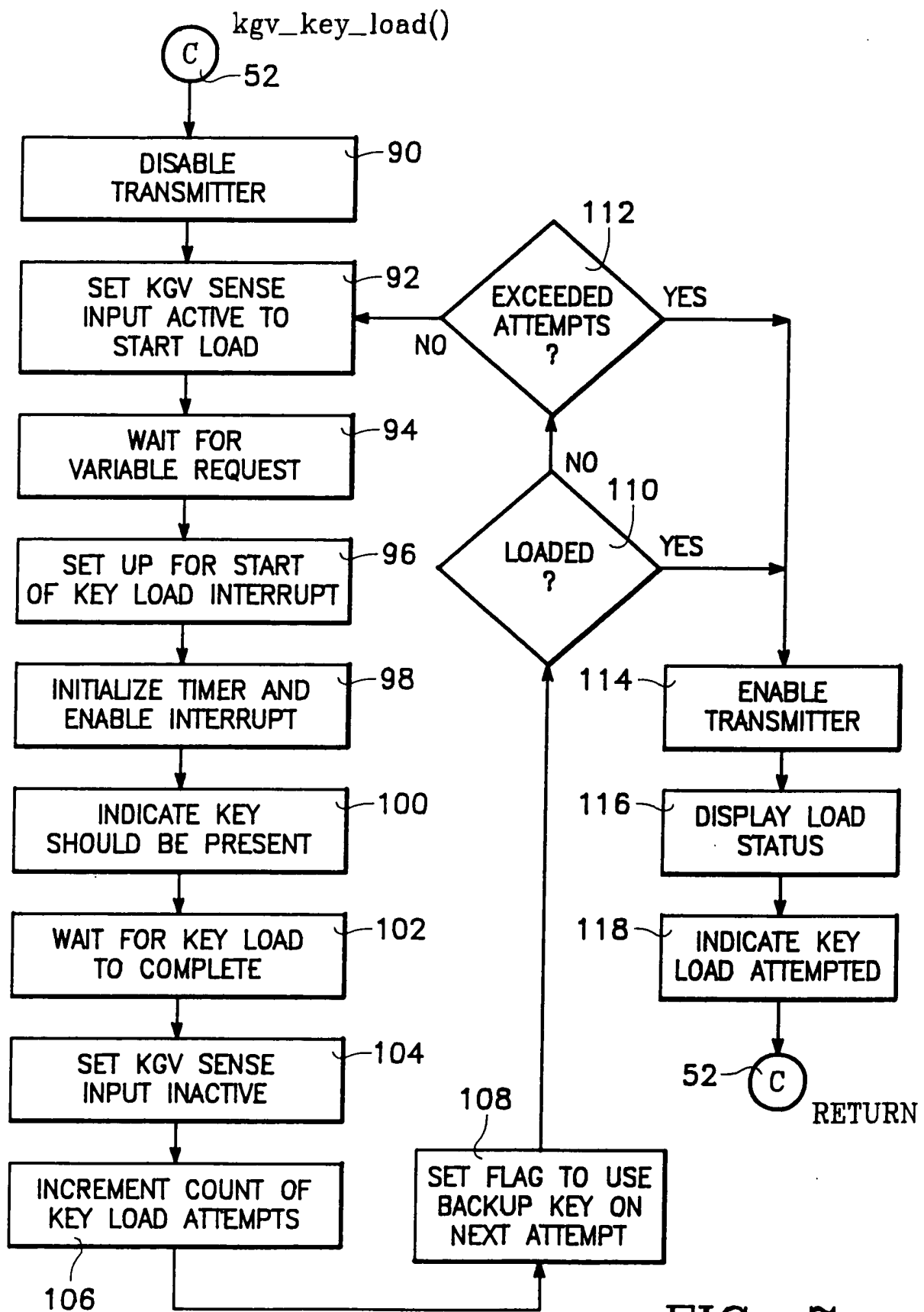


FIG. 7

002720-0E850560

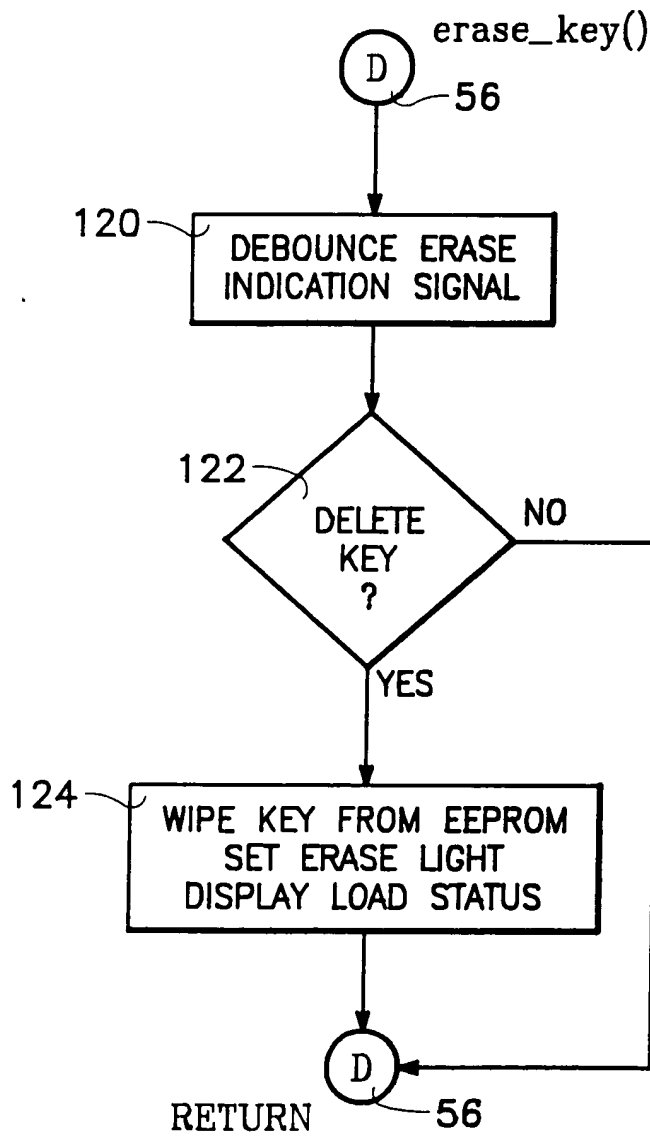


FIG. 8

interrupt handler()
INTERRUPT 130

CLEAR TIMER
INTERRUPT FLAG 132

FIG. 9

